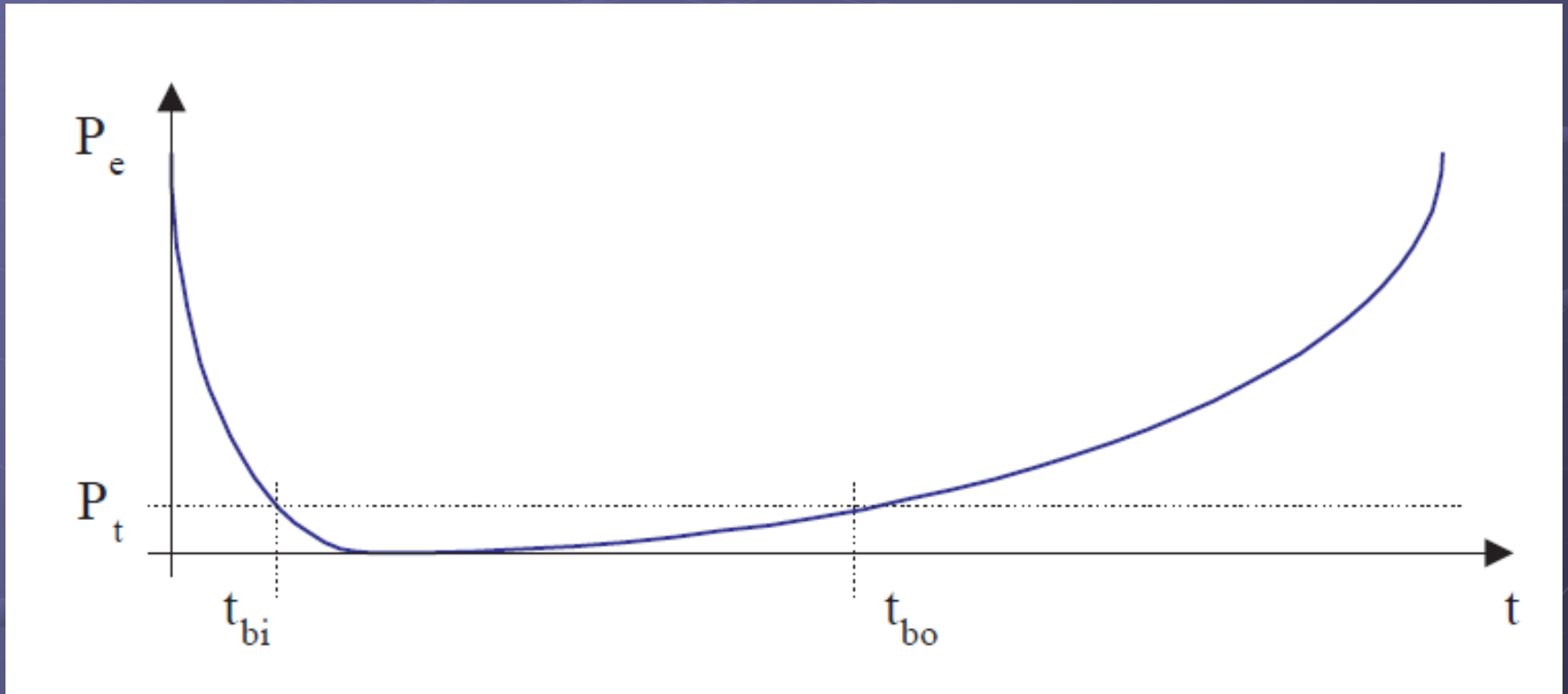


Interactive monitoring of Electronic Circuits with Embedded Microcontroller

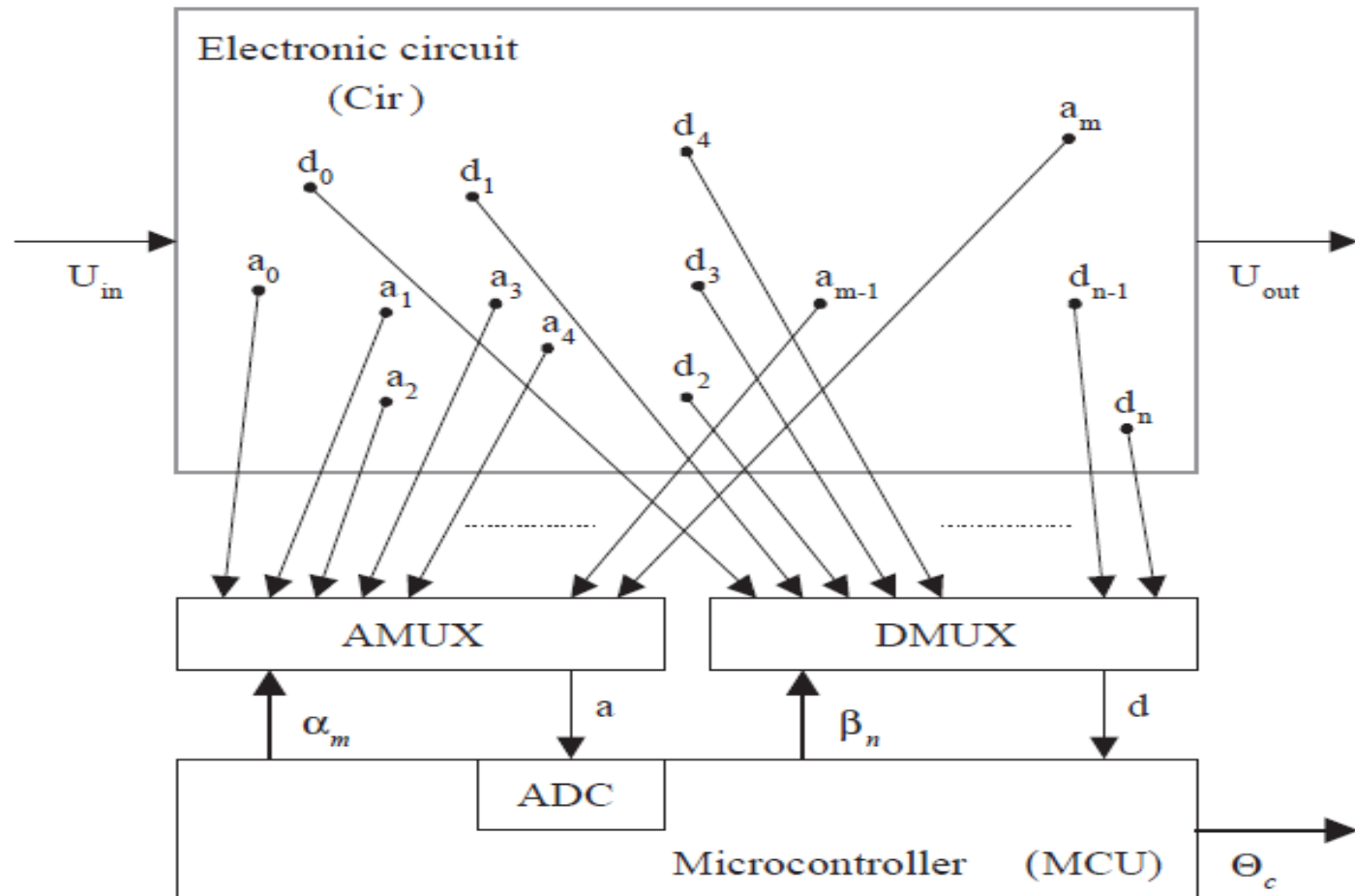
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gyorok.gyorgy@amk.uni-obuda.hu

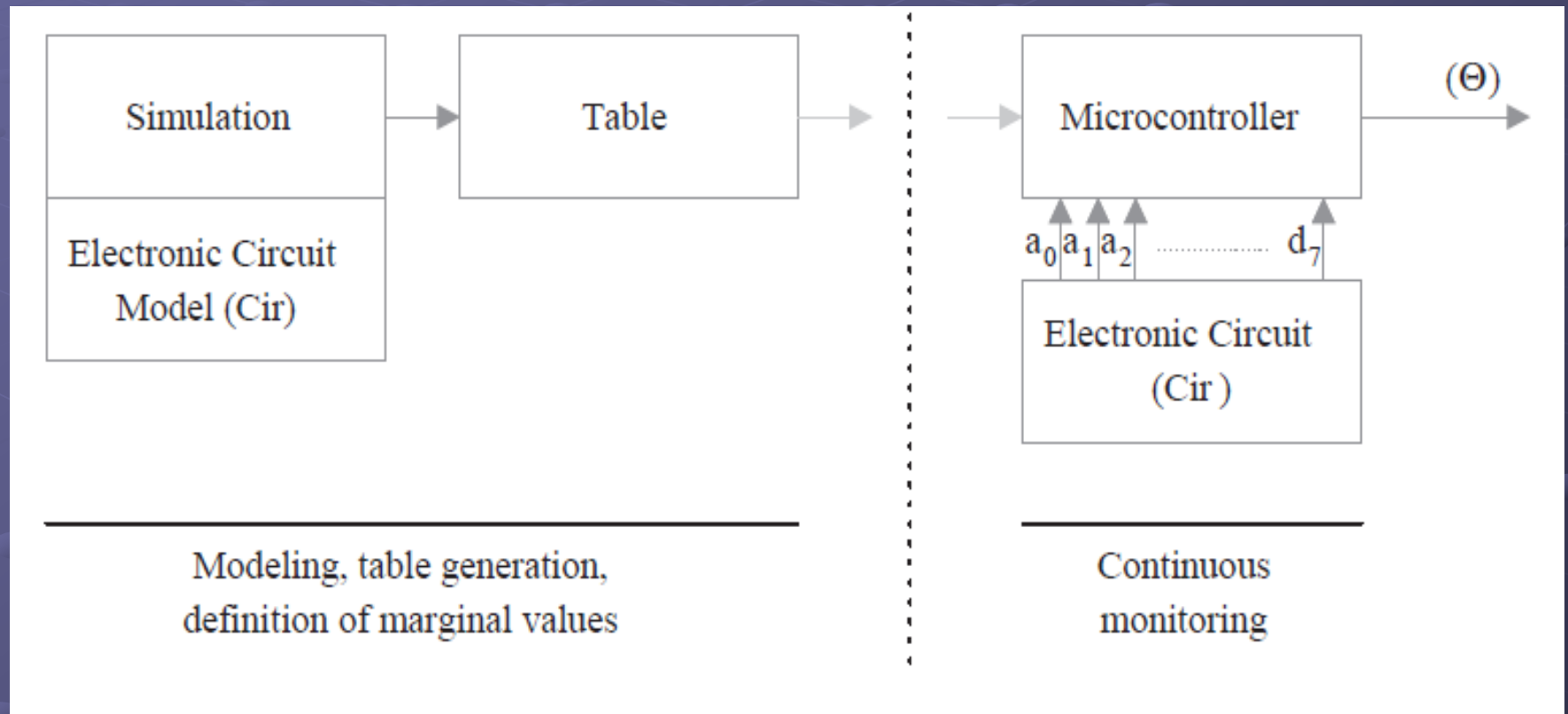
Probability of failure as time function of a typed electronic unit



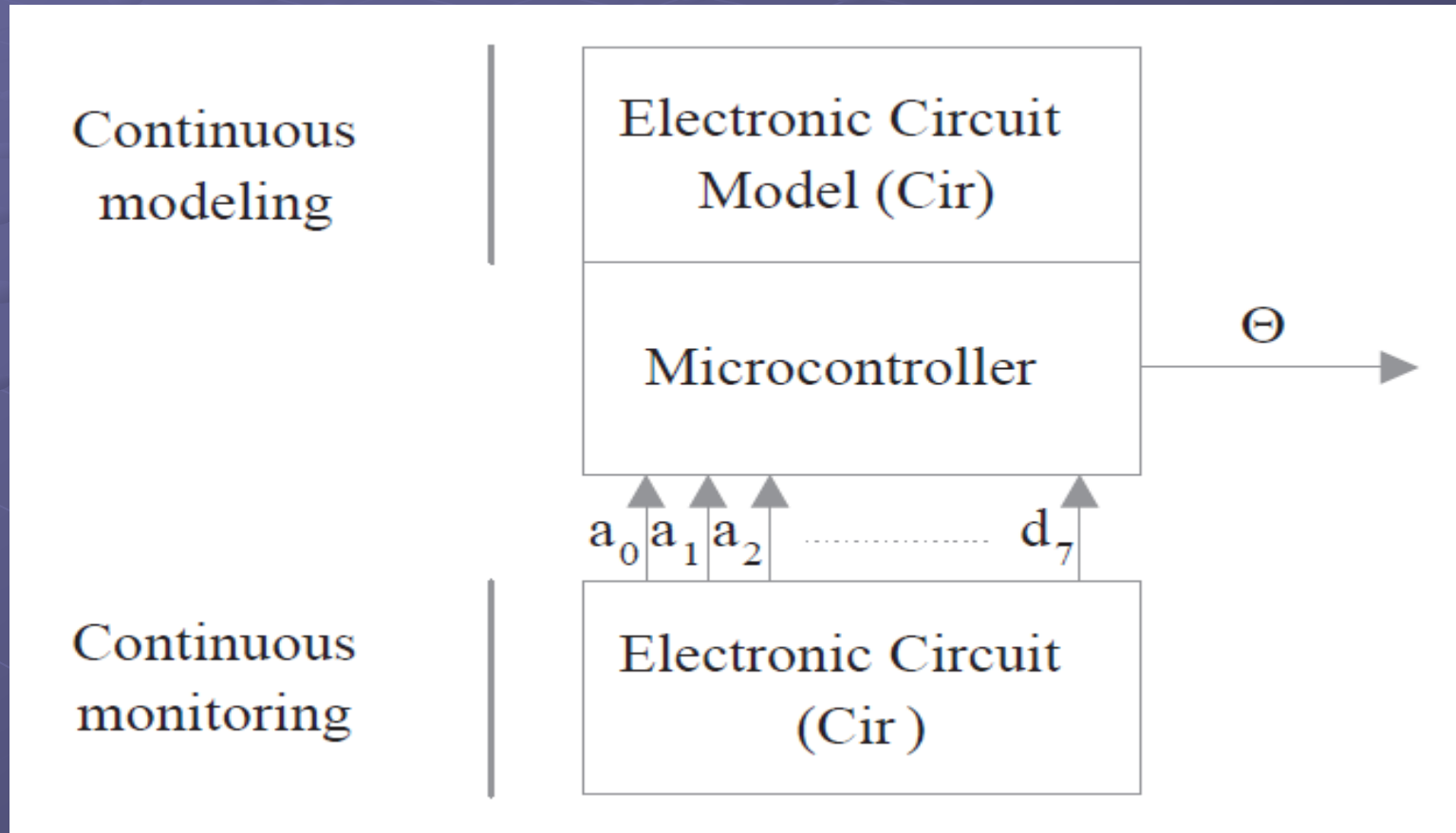
Theoretical testing of a hybrid electronic circuit



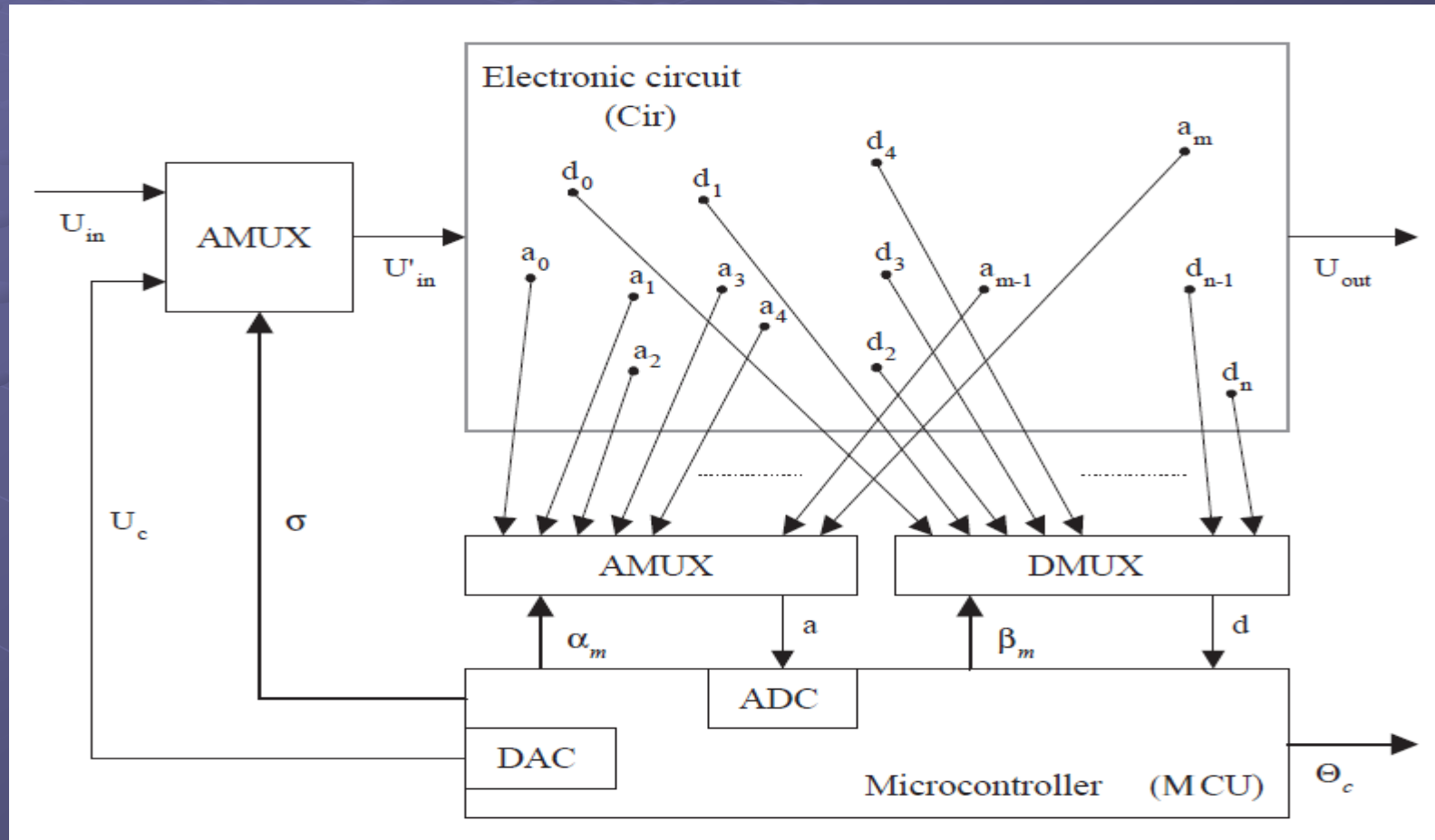
Simulation and then continuous testing of an electronic circuit



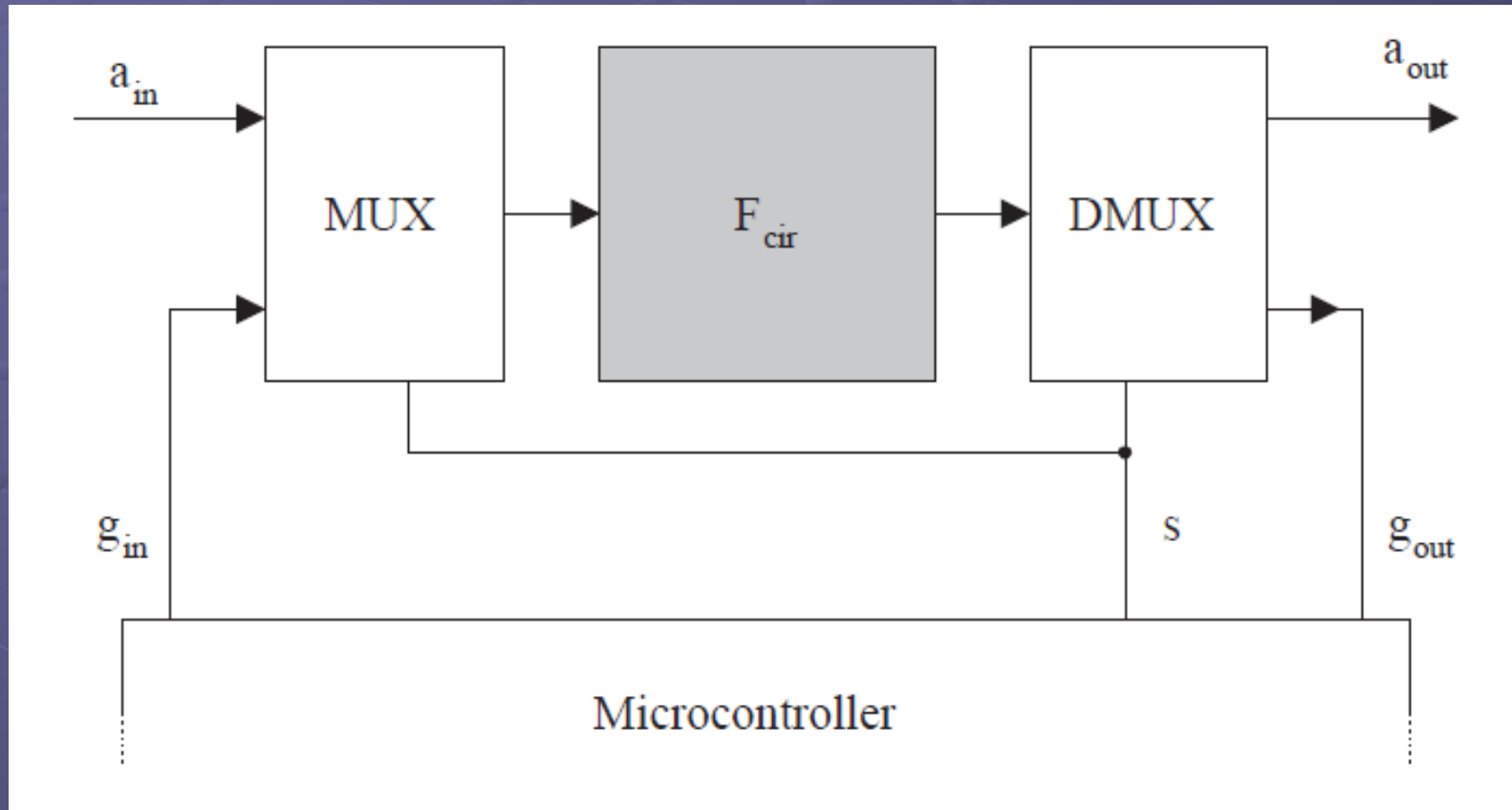
Continuous simulation and testing of an electronic circuit



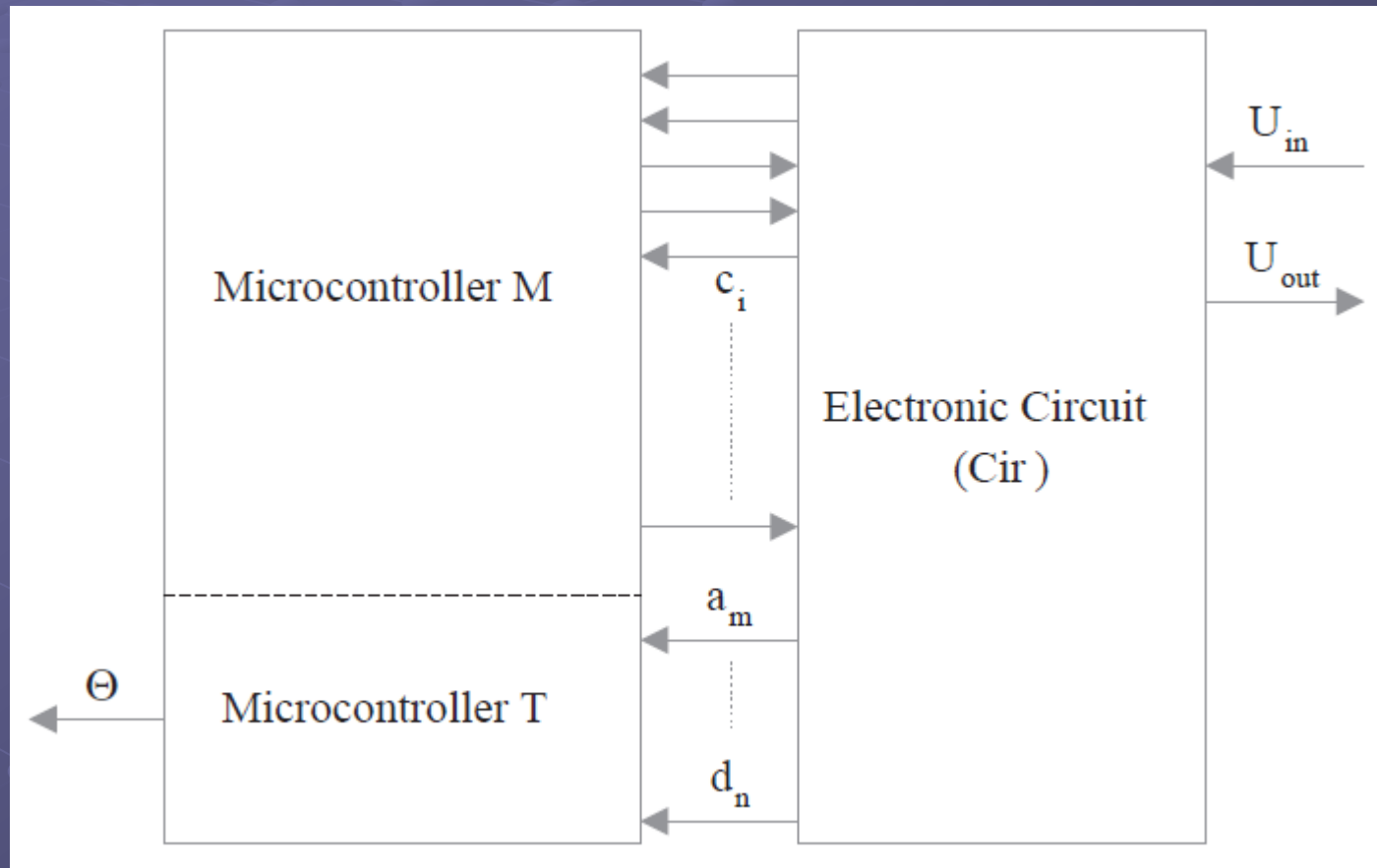
Examination of a hybrid circuit using an input stimulator



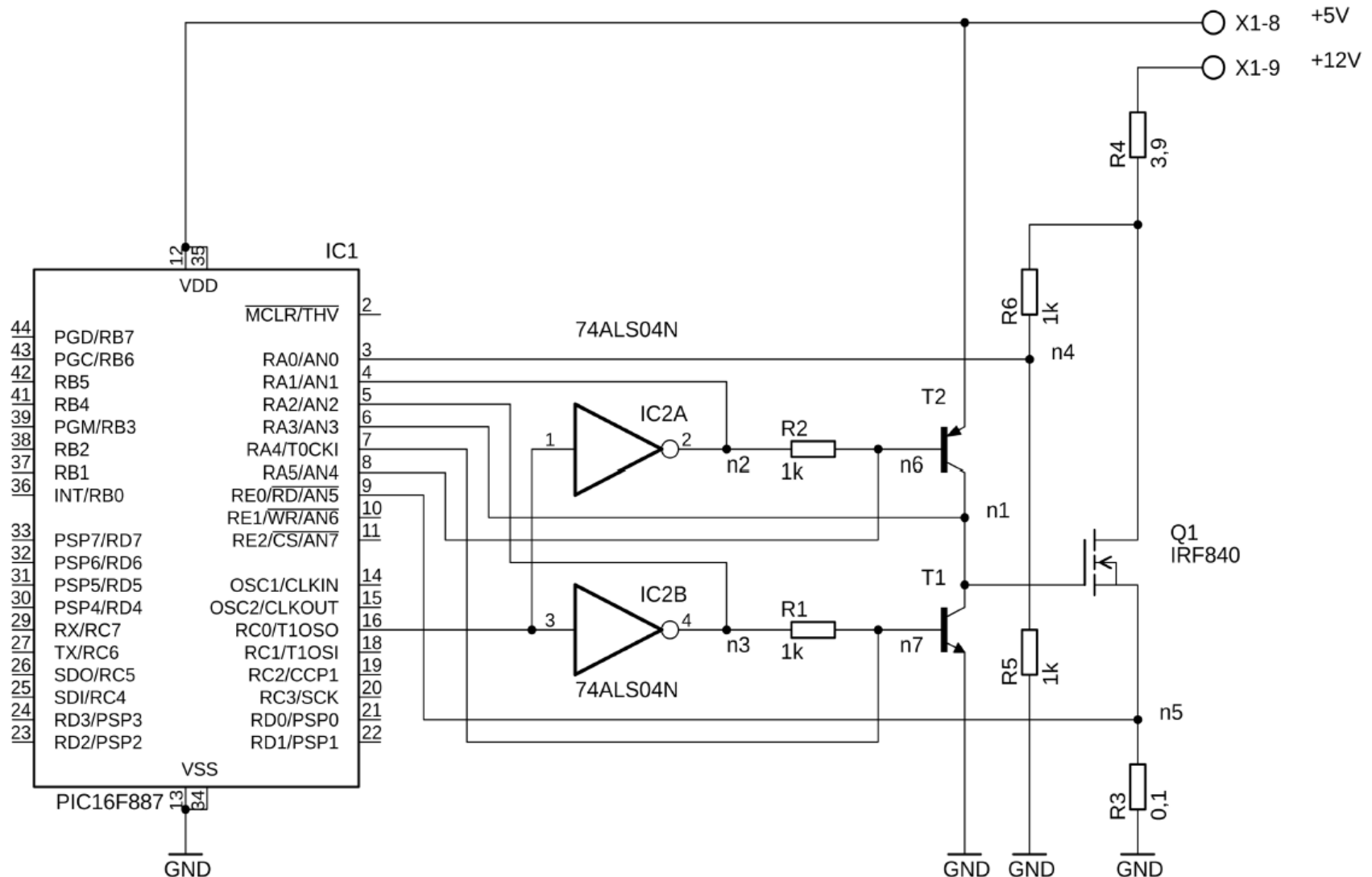
Theoretical possibility of interactive testing



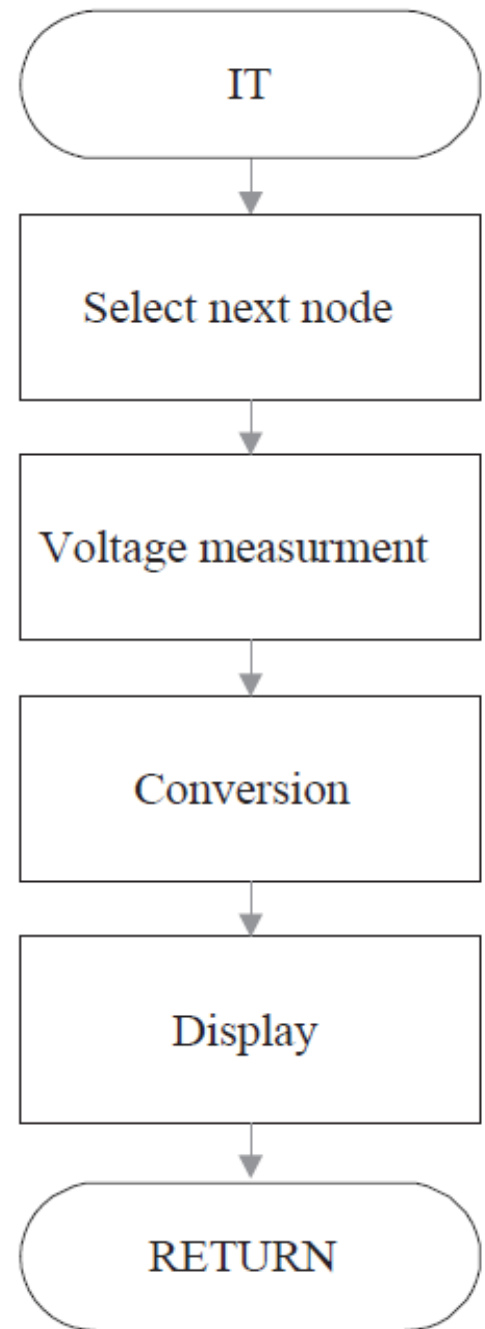
Examination of a hybrid circuit using an input stimulator



Experimental hybrid circuit



Flowchart of an interrupt subroutine for voltage measurement



Display of the voltage values of each measuring point for a control state



Thank you for your
attention

Good Good